32-bit PIC® and SAM Microcontrollers Peripheral Integration

Quick Reference Guide

	Core																				P	eriph	eral F	unctio	n Foo	cus														
		Max. Operating Frequency (MHz)	(511)	RAM (KB)		Inte	elligen	t Analo	g		veforr ontrol		Timino Measure			ty and				C	ommu	ınicati	ions				Use	r Interf	ace		Security					System Flexibility				
Product Family			Program Flash Memory (KB)		Pin Count	ADC (channels/bits)	ADC Speed (sps)	DAC (channels/bits)	Analog Comp. (+ 0 p Amp)	Output Compare Channels	Input Capture Channels	PWM Channels	16-bit/32-bit Timer	TCC (24-bit Control Timer) ⁽⁴⁾ Motor Interface (QEI/QDEC) ⁽⁴⁾	Watchdog Timer Dead Man Timer (DMT) ⁽⁴⁾	Class B Safety/DSU/ Touch Safety	USB (FS/HS) + PHY (Tx)	CAN (2.0B or FD)	Ethernet (10/100)	SERCONI/FLEACONIW	PC PC	ωldS	D/eMM(CMOS Camera Interface	Audio CODEC (I2S)(4)	Peripheral Bus Interface PMP/EBI (Bus width, bit) ⁽⁴⁾	Touch PTC (channels)/Driven Shield + (4)	Segment/Graphics LCD Controller	LCD/GFX Interface (PMP/EBI)	Crypto Engine (AES, SHA, ECC, RSA/DSA, TRNG)			TrustRAM (Bytes) (4)	DataFlash (KB) ⁽⁴⁾	Jamper Detection Dual Panel/Bank Flash ⁽⁴⁾	Intelligent Low Power Peripheral Event System (channels)*	<u>(S</u>	Low Active Power (µA/MHz)	5V Supply	Ultra-Small Package (WLCSP)
PIC32 Family							0001						- 10																											
PIC32MM GPL	microAptiv™	25	16-64	4-8	20-36		200k		2		_		7/3		W					2	_	2			2													✓	· ·	
PIC32MM GPM*	microAptiv	25	64-256	16-32	28-64	24/12	200k	1/5	3	9	9	24	21/9		W		1 ^{F+P*}		_	3	3	3			3							_	_	_	\perp		4	✓	· ·	\perp
PIC32MX1/2*/5*+	M4K	50	16–512	4–64	28–100	48/10	1M		3	5	5	5	5/2		W	В	1 ^{F+P*}	1+		5	2	4			4	P ¹⁶			Р								4			\perp
PIC32MX1/2 4	M4K	72	128-256	32-64	28-44	13/10	1M		3	5	5	5	5/2		W+D	В	1 ^{F+P}			2	2	2			2	P ¹²			Р								4	✓		
PIC32MX3/4*	M4K	120		16-128	64-124	16/10			2	-	-	5	5/2		W	В	1F+P*			- 5					2	P ¹⁶			Р								4			
PIC32MX5	M4K M4K	80	64-512	16-64	64-100	16/10			2			5	5/2		W	B	1F+P	1	1	6	5 5	4				P16			P P								8			
PIC32MX6 PIC32MX7	M4K	80	64–512 128–512	32-128	64-100	16/10			2			5	5/2		W	В	1 F+P		1		5 5					P16			Р								8			
				32–128						-	-			-					1																1					
PIC32MKGP/MC	microAptiv	_	512-1024	128-256	64-100	42/12		3/12	504		-	16	14/16	E	W+D	В	2 ^{F+P}	4		6		6			6	P ²⁴			P	=					✓ ✓		13			
PIC32MZEF ⁽³⁾ PIC32MZDA ⁽²⁾	M-Class microAptiv		512-2048 1024-2048		160_288	48/12			2				9/4		W+D W+D	B	1 ^{H+P}	2	1	6	5 5	-	1		6	P/E ²⁴		G	P+E P+E	A,S,T A,S,T					V		18 26			
SAM Family	microAptiv	200	1024-2040	230-040	103-200	40/12	TOW		- 1	9	9	<u>ه</u> ا	3/4		WTD			2			, 5	10			10	1 / L		l G	I TL	A,0,1							20			
SAMD09	CM0+	48	8-16	4	14-24	10/12	350k			6	3	4	2/1		W	В			2	2 2	2	2														6	6	✓		
SAMD10	CM0+	48	8–16	4	14-24	10/12	350k	1/10	2	6	3	12	2/1	1	W	B+T			3	3 3	3	3					P ⁷²									6	6	✓		✓
SAMD11	CM0+	48	16	4	14-24	10/12	350k	1/10	2	6	3	12	2/1	1	W	B+T	1 ^{F+P}		3	3 3	3	3					P ⁷²									6	6	✓		✓
SAMD20	CM0+	48	16-256	2-32	32-64	20/12	350k	1/10	2	16	8	16	5/2		W	B+T			(6	6	6					P ²⁵⁶									8		✓		✓
SAMD21	CM0+	48	32-256	4-32	32-64	20/12	350k	1/10	2	18	8	24	5/2	3	W	B+T	1 ^{F+P}		6	6	6	6			1		P ²⁵⁶									12	12	✓		✓
SAMD21L	CM0+	48	32-64	4–8	32-48	18/12	350k	1/10	4	18	13	24	5/2	3	W	B+T			5	5 5	5 5	5														12	12	✓		
SAMDA1(3)	CM0+	48	16–64	4–8	32-64	20/12	_	1/10	2		_		5/2	3	W	B+T	1 ^{F+P}		6			-			1		P ²⁵⁶									12	8	1		
SAML10	CM23	32	16-64	4-16	24-32	10/12		1/10	203			_	3/1		W	B+T			3	3 3	3	3					P100, D+					~	256	2 .		8	8	✓		· /
SAML11	CM23	32	16-64	8-16	24-32	10/12	1M	1/10	203	6	6	6	3/1		W	B+T			3	3 3	3	3					P100, D+			A,S,T	✓	< v	256	2 "		8	8	✓		'
SAML21	CM0+	48	32-256	4-32	32-64	20/12	1M	2/12	203			24		2	W	B+T	1 ^{F+P}		6	6	6	6					P ¹⁶⁹			A,T						12	16	√ VBAT		· •
SAML22	CM0+	32	64-256	8–32	48–100	20/12	1M		2	12		12		1	W	B+T	1 ^{F+P}		6	6	6	6					P ²⁵⁶	S ³²⁰		A,T				٧		8	16	√ VBAT		· •
SAMC20	CM0+	48	32-256	4/32	32-64	12/12			2		-	18	-	2	W	B+T					4						P ²⁵⁶									6	6	1	1 1	
SAMC21 ⁽⁵⁾	CM0+	48	32-256	4–32	32-100	20/12		1/10	4			24		2	W	B+T		2 ^{FD}	8		_						P ²⁵⁶									12	12	√	1 1	· •
SAM4N	CM4		512-1024	64-80	48–100	16/10		1/10			-	4	2/-	D	W					3/		4															23	√		
SAM4S	CM4		128–2048		48–100	16/12		2/12	1			4	2/-	D	W		1 ^{F+P}			_	2 2	-	' '	/	1	E ²⁴			Е						√	14	22	V		✓
SAM4E	CM4F		512-1024	128	100-144	24/12		2/12	1			4	-/3	D	W		1 ^{F+P}	2	1	2/		-	' '	/		E ²⁴			Е	Α				٧			33	1		
SAM4L	CM4	48		32-64	48–100		300k	1/10	4			5	2/-		W		1 ^{F+P}			4/		5		/	1		P ³²	S ¹⁶⁰		A,T						4	16	V		V
SAMG	CM4F	120		64–176	49–100	8/12		0116			-	6	2/-		W		1F+P		8					, ,	2											6	30	1		· /
SAMD5x	CM4F		256-1024		64-128	32/12		2/12				24		2 D	W	B+T	1F+P	900	8		-	8	-	/ /	- '		P ³²			A,S,E,R,T					\ \ \ \	32	32	1	\ \ \	
SAME5x	CM4F		256-1024		64-128	32/12		2/12				24	G	2 D	W	B+T	1F+P	2 ^{FD}	1 8				-	1 1	-	F24	P ³²		-	A,S,E,R,T				· ·	_	32	32	1	-	
SAMS7x ⁽²⁾	CM7		512-2048			24/12			1			8	4/-	D	W		'	OFD			5 3		<u>'</u>	\ \ \ \	-	E ²⁴			E	A,S,T				,	_	12	24	√		
SAME7x ⁽²⁾	CM7		512-2048			24/12			1			8	4/-	D	W		'	2 ^{FD}	1	3/			'	/ /	-	E ²⁴			E	A,S,T				· ·	_	12	24	✓ ✓		
Note 1: USARTs wi	CM7		512-2048		Note 2: D	24/12				44 :			4/-	D	W	d or 100		-	1	_	5 3					E ²⁴	Note 1	2. Autor	E	A,S,T Grade Devid	200	NI.	oto 4.		nology	12 in the	24	•		

Note 1: USARTs with SPI mode are taken into account

Note 2: DRAM Memory Support: PIC32MZ DA with DDR2 (32MByte embedded or 128MB external); SAM S7x/E7x/V7x with SDRAM (external)

Note 3: AL

Note 3

Note 3: Automotive Grade Devices

Note 4: Terminology in the back



Terminology

TIMING AND MEASUREMENTS:	Signal Measurement with Timing and Counter Control									
TCC: Timer/Counters for Control	Selected SAM products have TCCs for applications like Switch Mode Power Supplies (SMPS), lighting and motor control. The TCCs support up to 96 MHz and 24-bit resolution.									
QEI: Quadrature Encoder Interface QDEC: Quadrature Decoder	QEI to increment encoders for obtaining mechanical position data typical for automation or motor control applications. QDEC performs the input lines filtering, decoding of quadrature signals and connects to the timers/counters in order to read the position and speed of the motor through the user interface.									
SAFETY AND MONITORING: Ha	rdware Monitoring and Fault Detection									
DMT: Dead Man Timer	The primary function of the DMT is to reset the processor in the event of a software malfunction. A DMT is typically used in mission-critical and safety-critical applications, where any single failure of a software functionality and sequencing must be detected.									
COMMUNICATIONS: General, Inc	dustrial, Lighting and Automotive									
SERCOM: Serial Communication Module	The SERCOM is software that is configurable to operate as I°C, SPI or USART, giving you extended flexibility to mix serial interfaces and greater freedom in PCB layout. Each SERCOM instance can be assigned to different I/O pins through I/O multiplexing, further increasing versatility.									
I2S: Inter-IC Sound Controller	The Inter-IC Sound Controller provides a bidirectional, synchronous digital audio link with external audio devices.									
PMP: Parallel Master Port EBI: External Bus Interface	An embedded peripheral touch controller makes it easy to add capacitive touch sensing to your project with buttons, sliders, wheels and proximity. By offering superb sensitivity and noise tolerance as well as self-calibration, the PTC eliminates the need for external components and minimizes CPU overhead. The PTC supports up to 256 channels on 64-pin devices, 120 channels on 48-pin devices and 60 channels on 32-pin devices. PTC with Driven Shield + can achieve better noise immunity and moisture tolerance.									

Development Tools

MIPS-Based PIC32 Products

WIFS-based FIGS2 Froducts							
Tool	Description						
MPLAB® X IDE	MPLAB X Integrated Development Environment (IDE) is for developing and debugging MIPS-based PIC32 MCU applications, in addition to Microchip's 8- and 16-bit PIC® microcontrollers. It is based on the open-source NetBeans IDE from Oracle and runs under Windows®, Mac OS® and Linux®, and connects seamlessly to a range of debuggers, programmers and development kits.						
MPLAB Harmony Configurator	The MPLAB Harmony Configurator (MHC) is a time-saving hardware configuration utility for MPLAB Harmony, Microchip's award winning software framework. You can use MHC to get visual understanding and control of the configuration of your target device and application. MHC is a fully integrated tool within MPLAB X IDE.						
MPLAB Harmony Software Framework	MPLAB Harmony is a flexible, abstracted, fully integrated firmware development platform for PIC32 microcontrollers. It takes key elements of modular and object-oriented design, and provides the option of adding in the flexibility of a Real-Time Operating System (RTOS). MPLAB Harmony provides a framework of software modules that are easy to use, configurable for your specific needs and in a format that allows for maximum reuse and reduces your time to market.						
MPLAB Harmony Graphics Composer	MPLAB Harmony Graphics Composer (MHGC) is Microchip's industry-leading Graphical User Interface (GUI) design tool for PIC32 microcontrollers. Providing a fully integrated easy-to-use WYSIWYG editor, graphics asset management and code generator within the MPLAB Harmony framework, the MHGC allows you to go from concept to glass in minutes without writing a single line of code. Additionally the integrated Display Manager plugin enables quick support for new and unsupported displays in MPLAB Harmony.						
Touch Interface	Capacitive and resistive touch screen support is an integrated part of the MPLAB Harmony Graphics Composer (MHGC). With automatic generation and configuration of event handlers for touch events, the MHGC allows quick development of touch enabled graphics solutions.						

USER INTERFACE: Capacitive To	uch Sensing and LCD Control								
PTC: Peripheral Touch Controller	An embedded peripheral touch controller makes it easy to add capacitive touch sensing to your project with buttons, sliders, wheels and proximity. By offering superb sensitivity and noise tolerance as well as self-calibration, the PTC eliminates the need for external components and minimizes CPU overhead. The PTC supports up to 256 channels on 64-pin devices, 120 channels on 48-pin devices and 60 channels on 32-pin devices. PTC with Driven Shield+ can achieve better noise immunity and moisture tolerance.								
SYSTEM FLEXIBILITY: System P	eripherals and Interconnects								
CLC/CCL: Configurable Custom Logic	The CCL is a programmable logic peripheral which can be connected to the device pins, events or to other internal peripherals. This allows you to eliminate logic gates for simple glue logic function on the PCB.								
EVSYS: Event System	The Event System allows autonomous, low-latency and configurable communication between peripherals. Several peripherals can be configured to generate and/or respond to signals known a events. Communication is made without CPU intervention and without consuming system resourc such as Bus or RAM bandwidth. This reduces the load on the CPU and other system resources, compared to a traditional interrupt-based system.								
Dual Panel/Bank Flash	Dual Bank Flash allows live field firmware/program update on one bank while CPU can continue executing code from another Flash bank.								
SECURITY: Chip-Level Security,	Crypto Acceleration, Secure Key Provisioning & Storage and Tamper Detection								
TrustZone	TrustZone® for ARMv8-M provides hardware-enforced security isolation between trusted and the untrusted resources on a Cortex™-M23 based device, while maintaining the efficient exception handling.								
TrustRAM	TrustRAM provides secure key storage and can resist microprobing and side channel attacks. It also prevents data remanence and facilitates rapid erase on tamper event.								
DataFlash	DataFlash provides secure key storage and can resist side channel attacks. It also allows data scrambling and facilitates rapid erase on tamper event.								
Secure Boot	Secure Boot authenticates the Flash content at startup and ensures the desired code is executed.								

Arm® Cortex®-M Based SAM Products

Tool	Description
Atmel Studio 7	Atmel Studio 7 is the Integrated Development Platform (IDP) for developing and debugging AVR® and Arm®-based SAM MCU applications. Atmel Studio 7 provides you with a seamless easy-to-use environment to develop and debug applications written in C/C++ or assembly code. It connects seamlessly to a range of debuggers, programmers and development kits.
Atmel START	Atmel START is an innovative online tool for intuitive, graphical configuration and deployment of embedded software. It lets you select and configure software components, drivers and middleware, as well as deploy complete example projects tailored to the needs of your application. It is completely platform independent, and able to generate project files for a number of IDEs. The configuration engine lets you review dependencies between software components and available hardware resources in the selected MCU, and automatically suggests solutions to any conflicts that in your chosen setup.
ASF Software Framework for SAM	ASF provides software drivers and libraries to build applications for AVR and SAM devices. It is architected for readability and performance, and contains a number of advanced middleware components for 32-bit SAM devices such as USB device, TCP/IP, Wi-Fi®, RTOS kernel (FreeRTOS), Bluetooth®, file system and more.
Data Visualizer	Track and profile your applications run-time behavior using the powerful Data Visualizer. It provides an oscilloscope view of signals such as GPIO, SPI, UART, etc. The Data Visualizer also provides live power measurements when used together with a supported probe or board, such as the power debugger. Profiling your applications power usage has never been easier.
QTouch® Composer	The QTouch Composer allows you to seamlessly develop capacitive touch functionality for your application. This simplifies the design process by tying together the tools required to edit the code in Atmel Studio 7 and tune the touch design in QTouch Composer.

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